

FIG. 1A

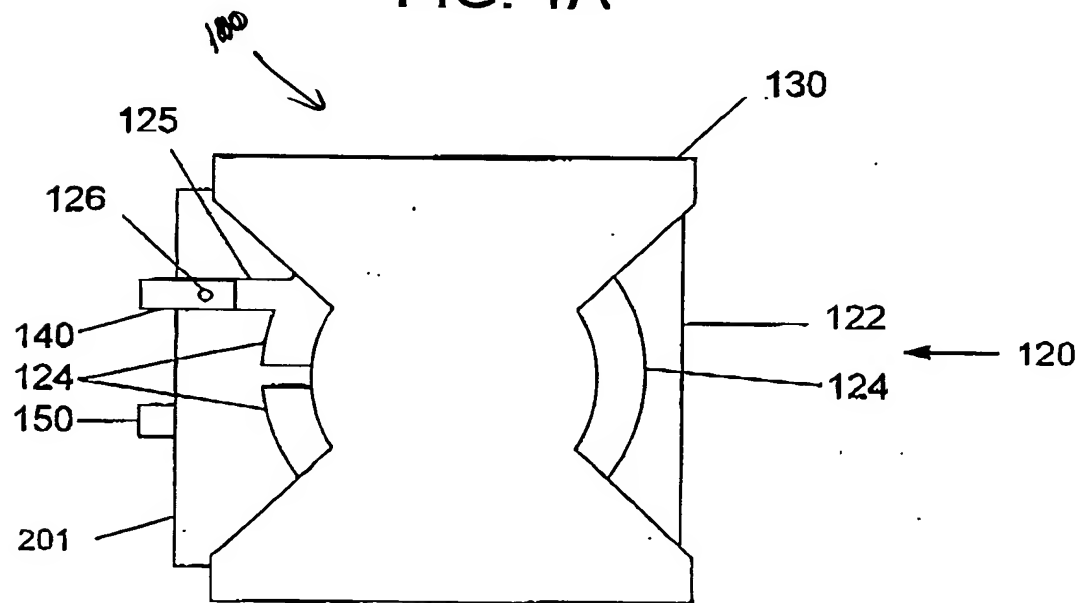


FIG. 1B

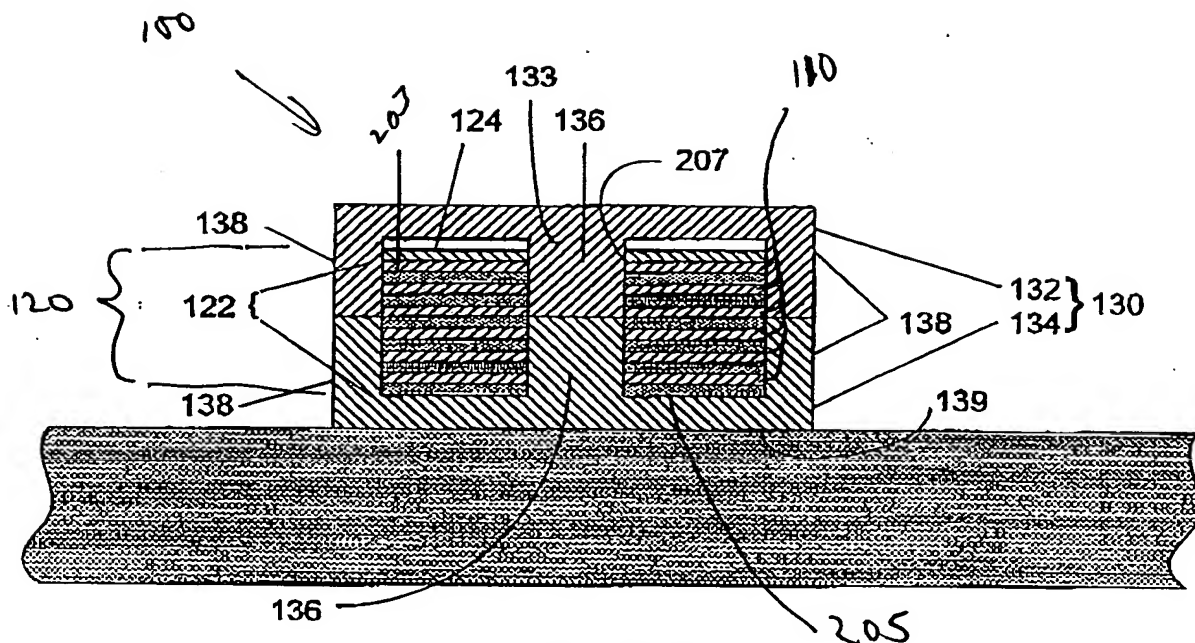


FIG. 1C

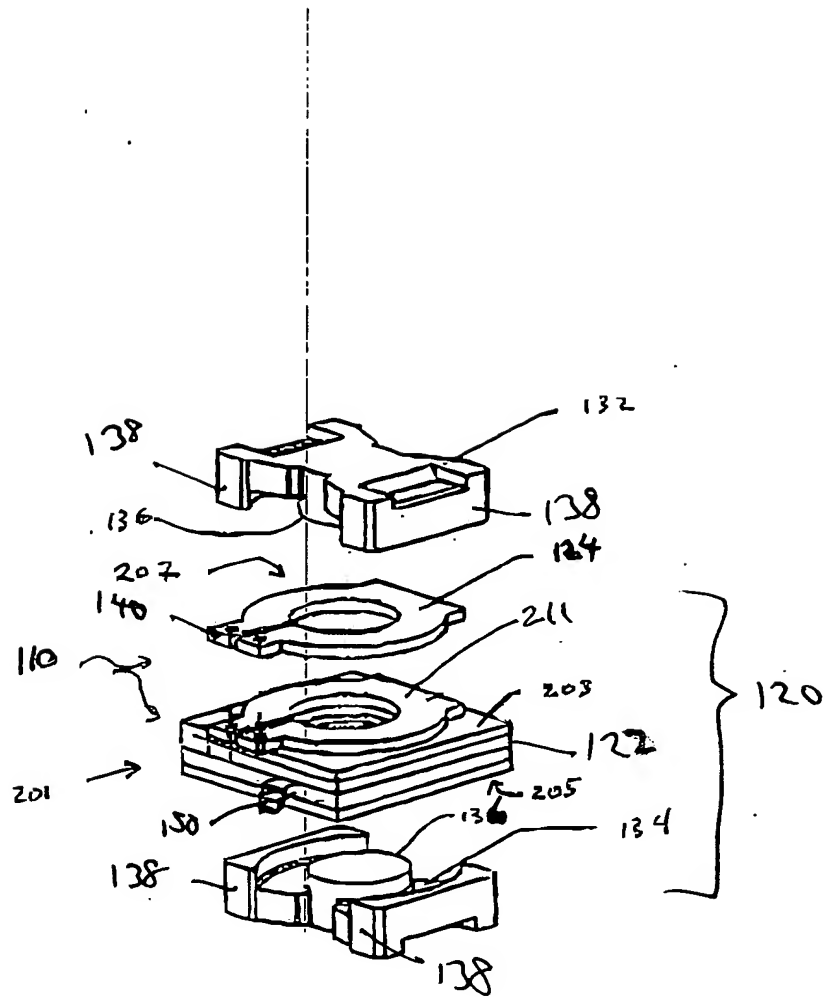


FIG. 2

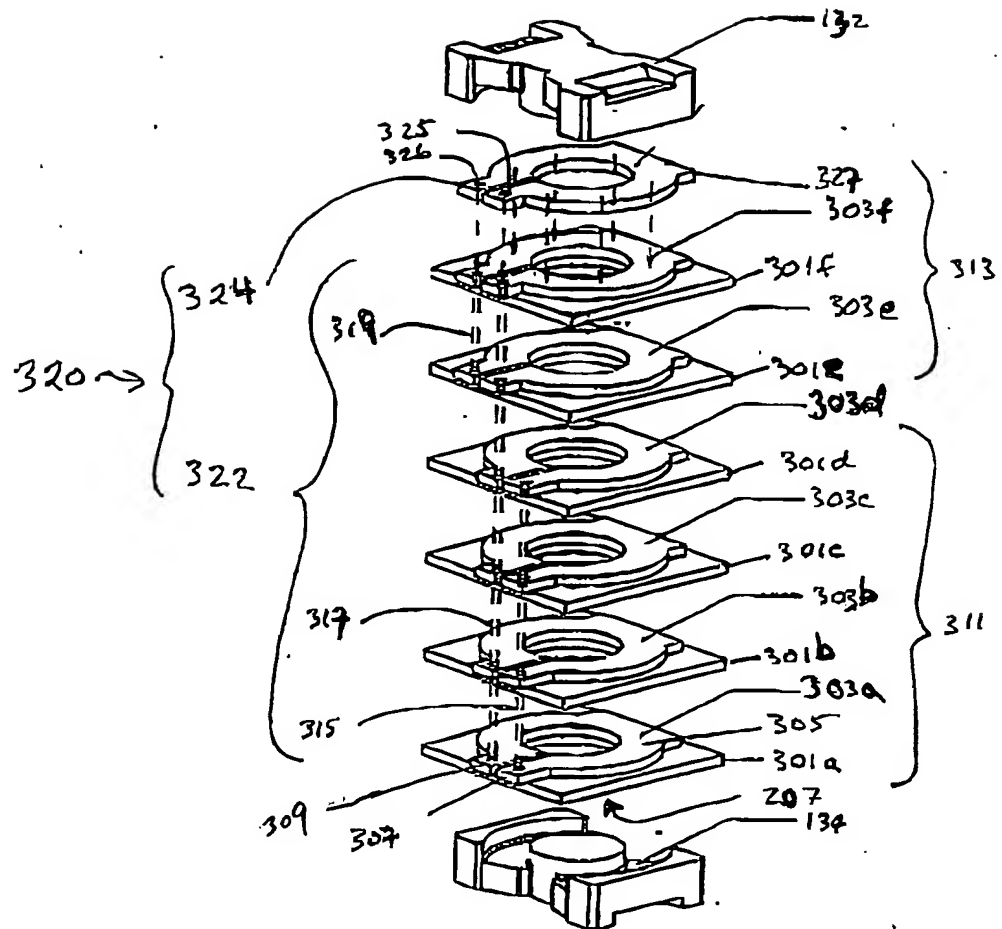


FIG. 3

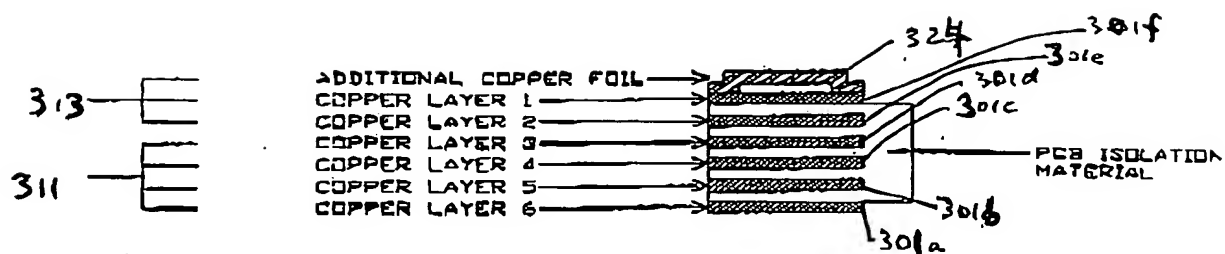


FIGURE 4

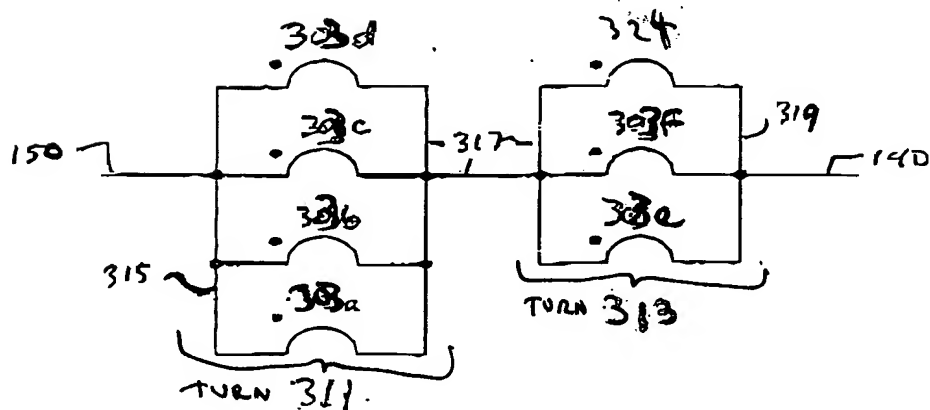
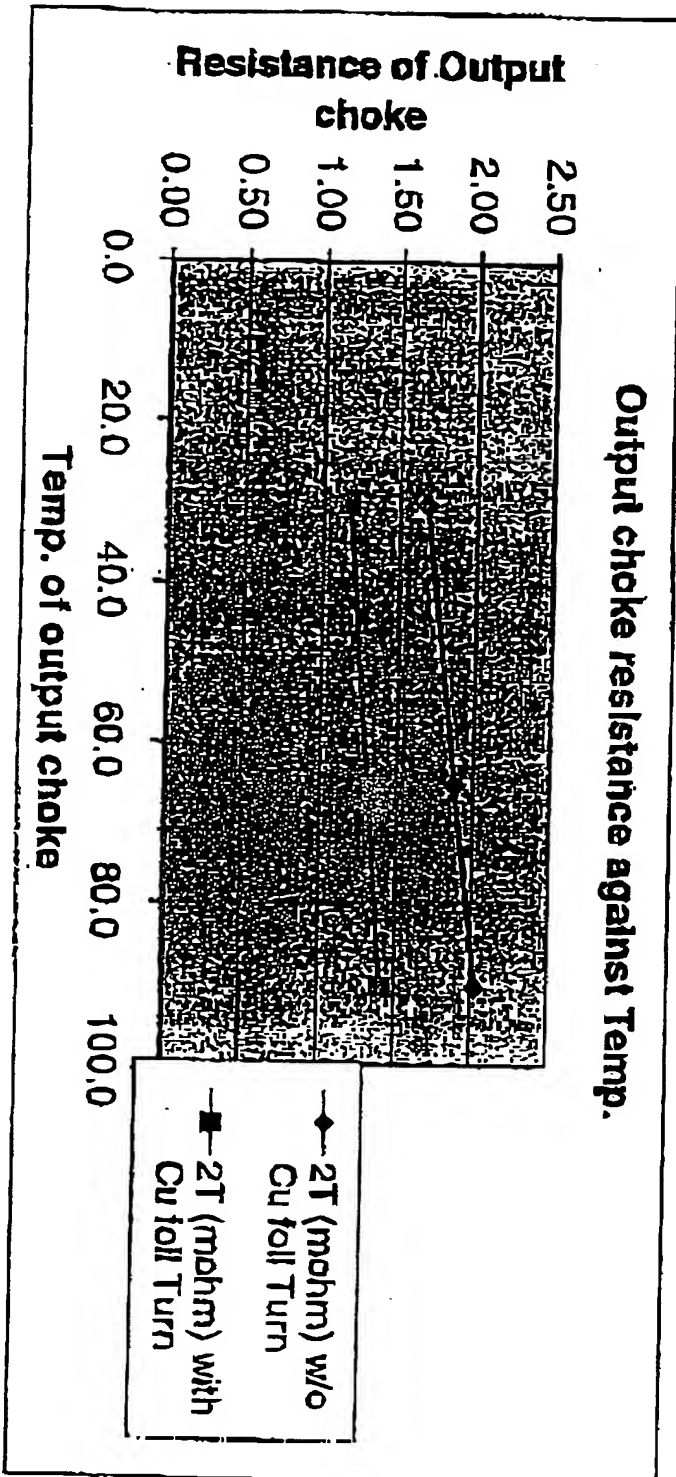


Fig. 5

Fig. 6

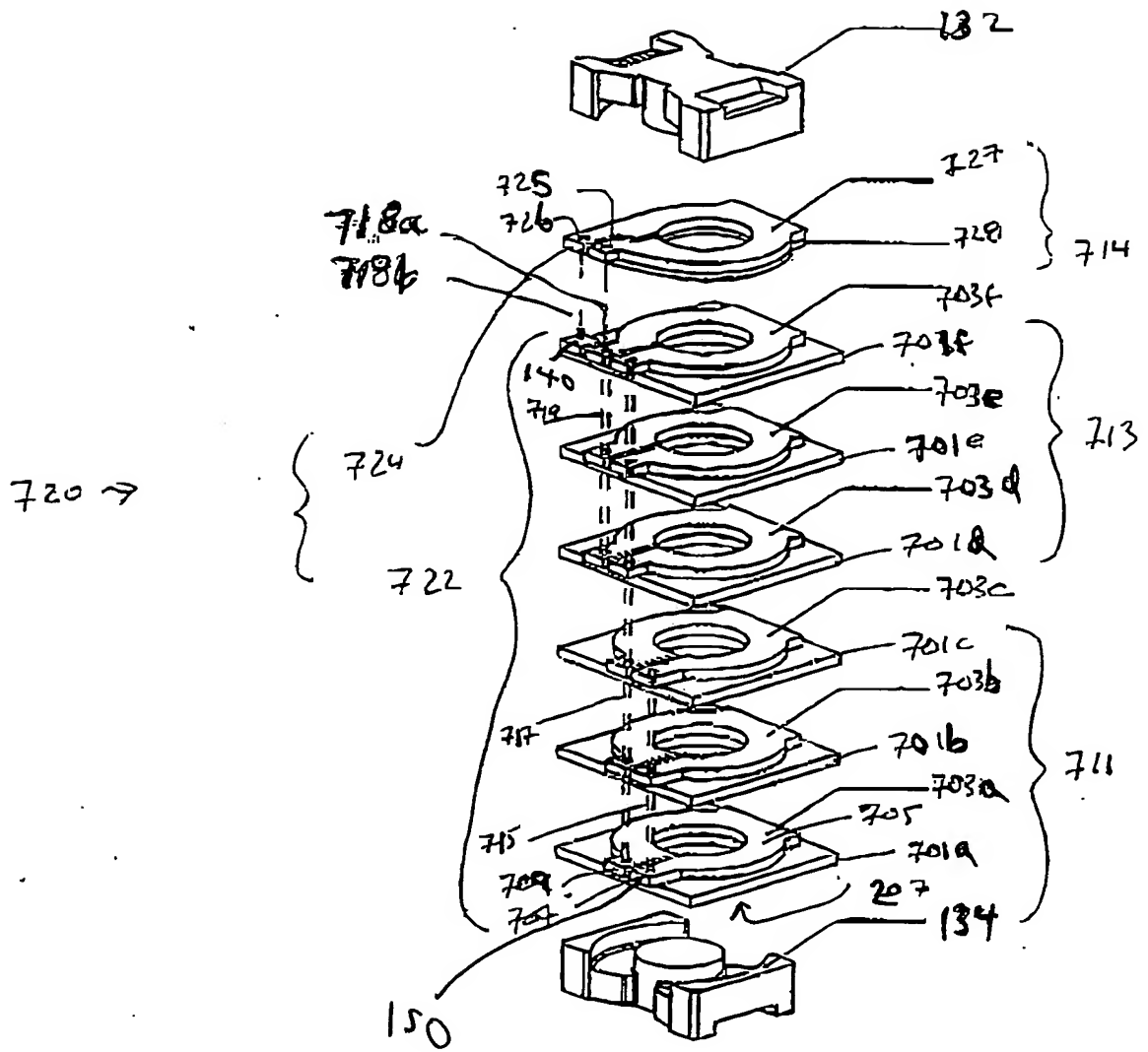


FIG. 7

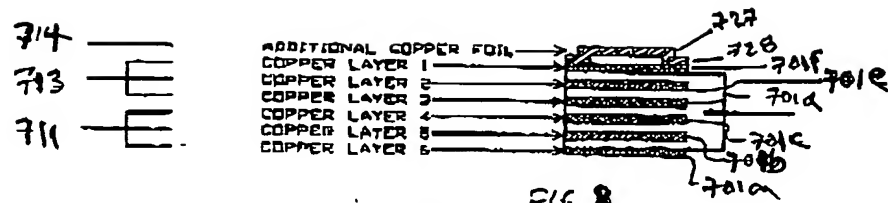


FIGURE 4. FIG. 8

6 Layer JT Output Choke Config.

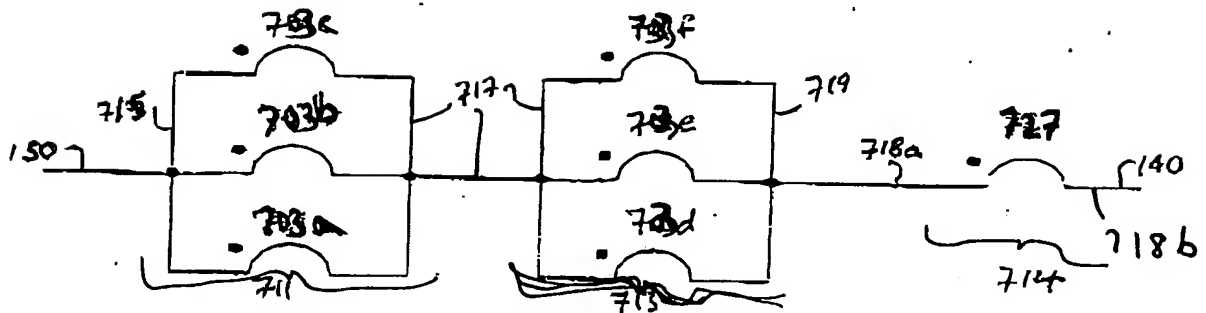


FIG. 9

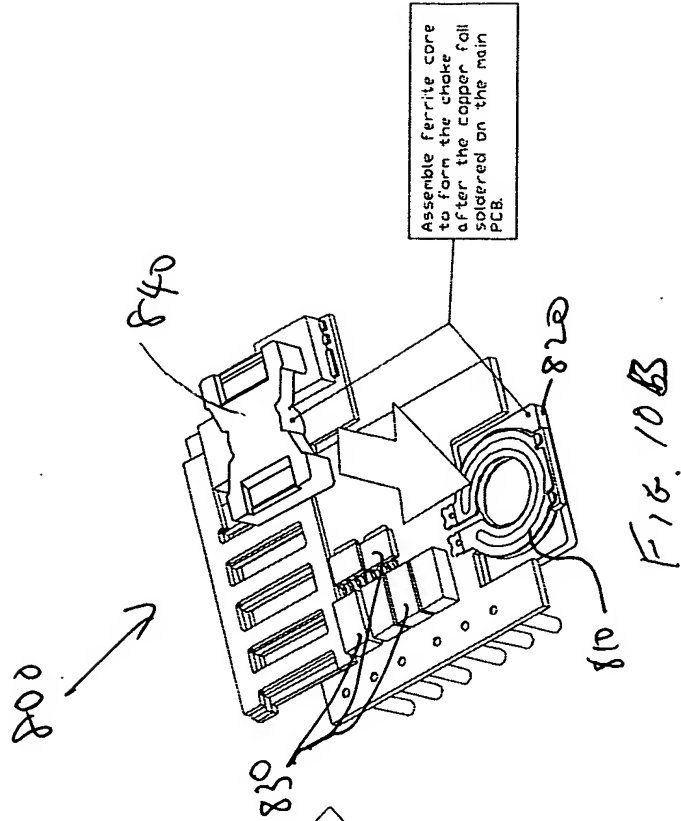


FIG. 10A

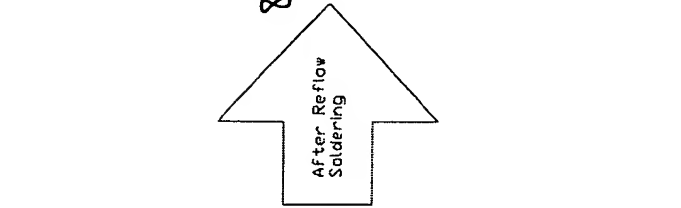


FIG. 10B

FIG. 10

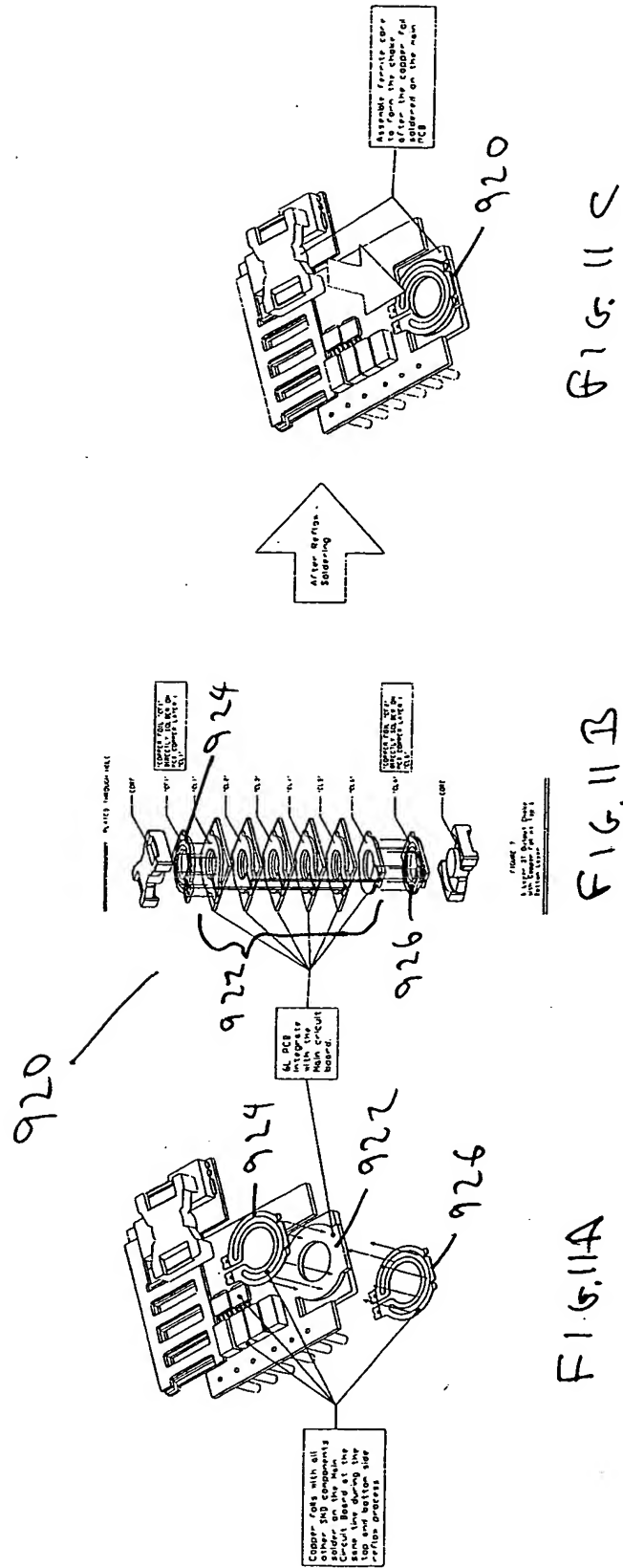


FIG. 11A

FIG. 11B

FIG. 11C

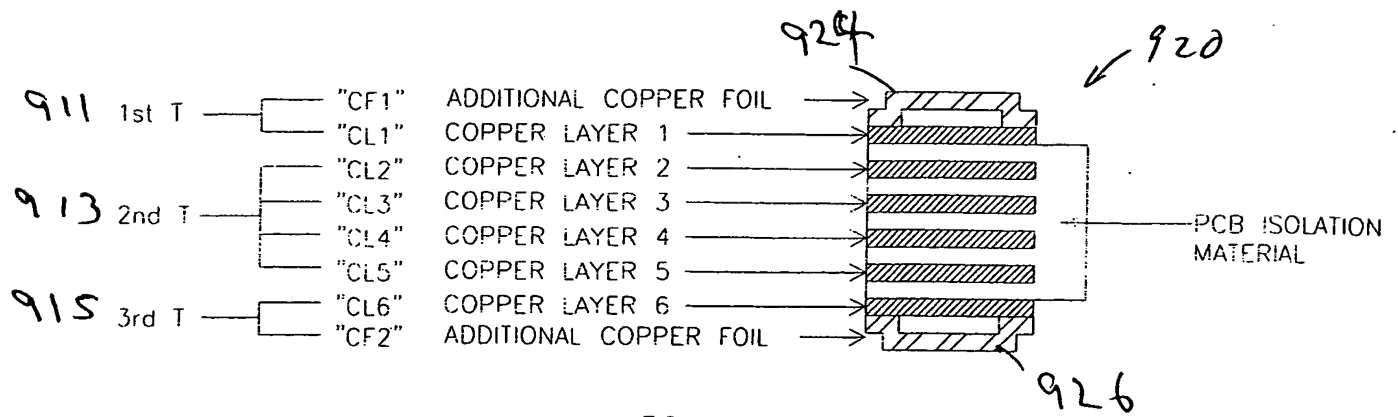


FIG. 12

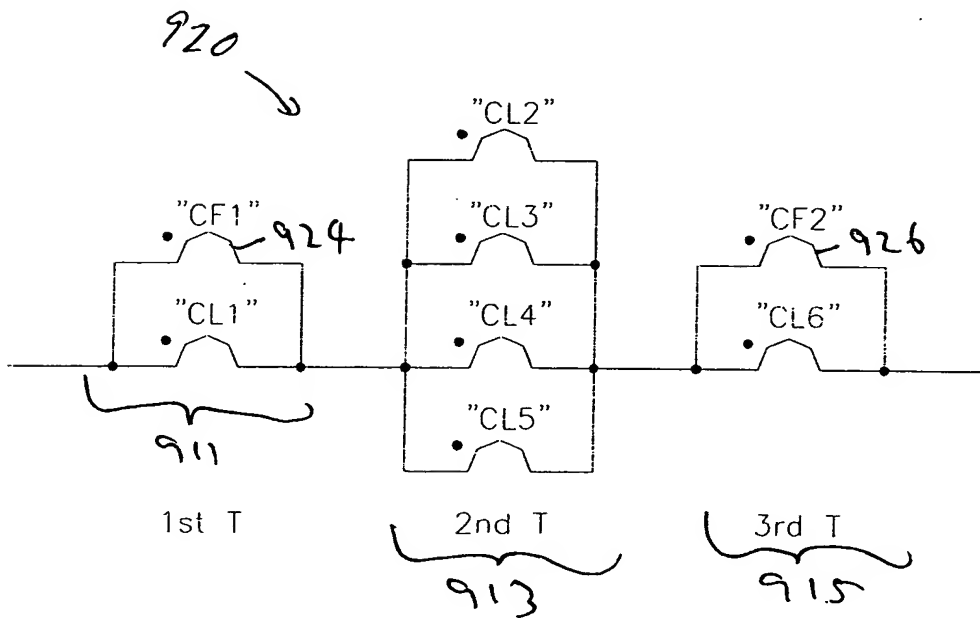


FIG. 13

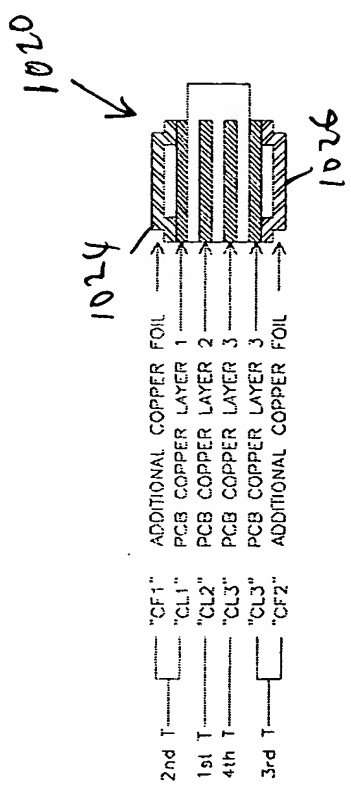


FIG. 15

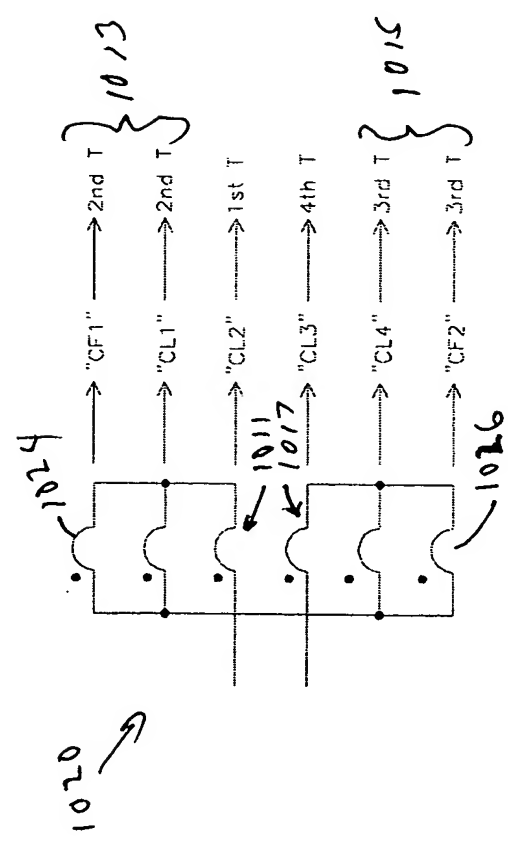


FIG. 16

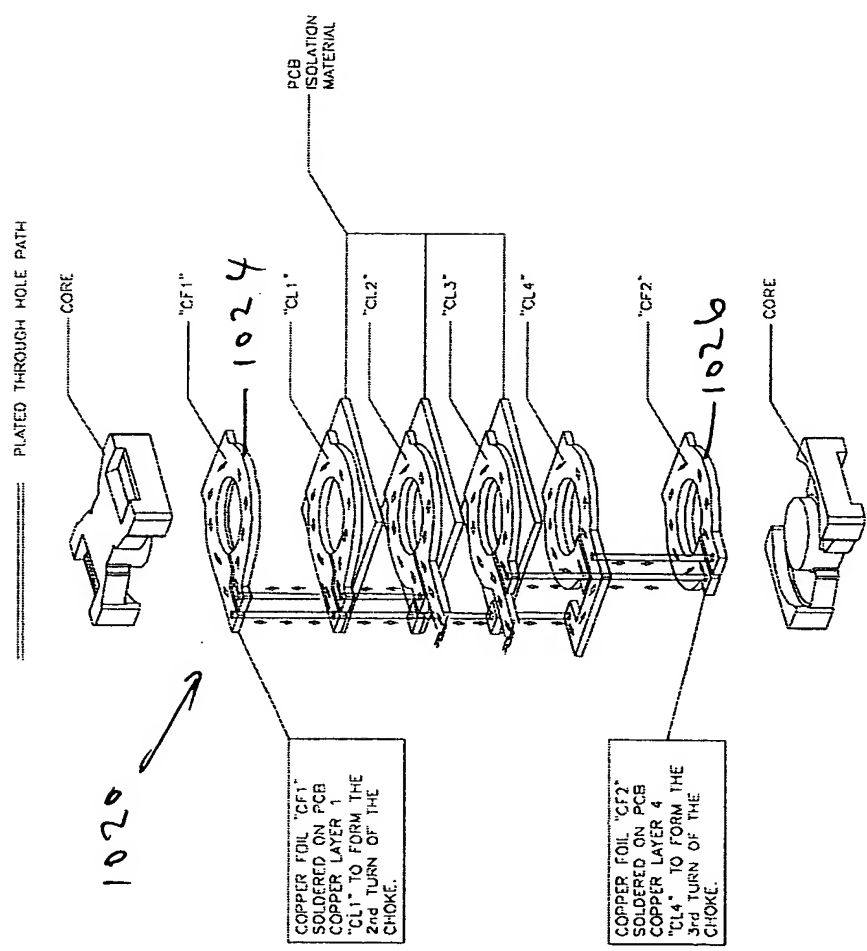
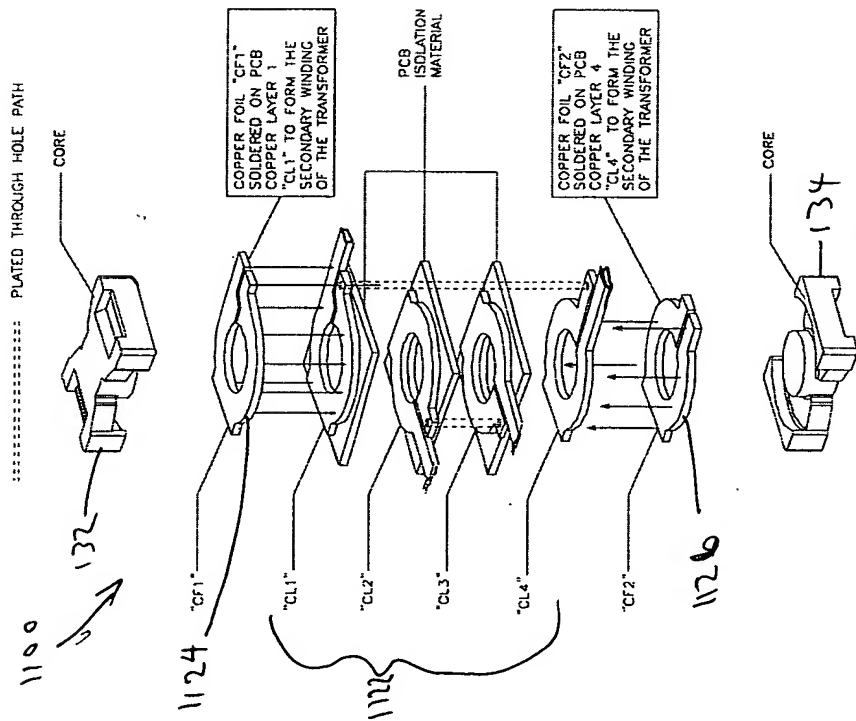
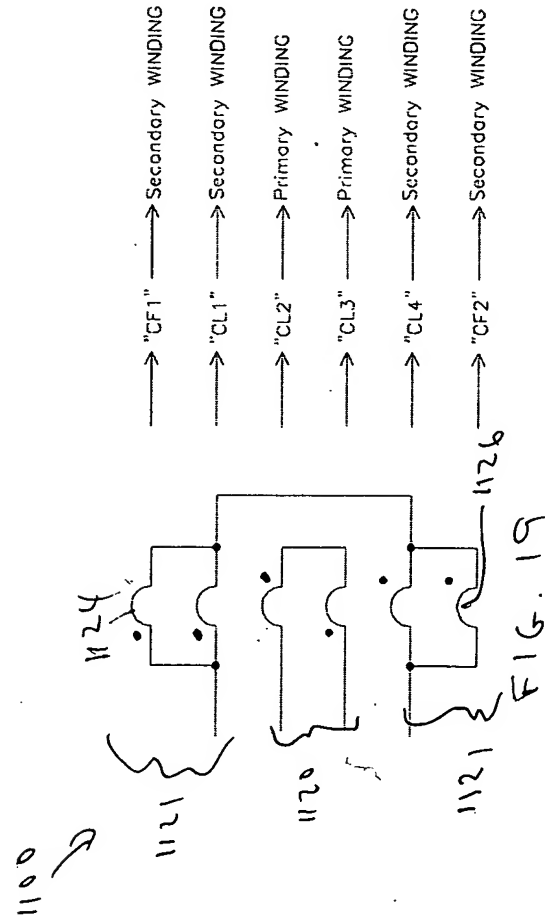
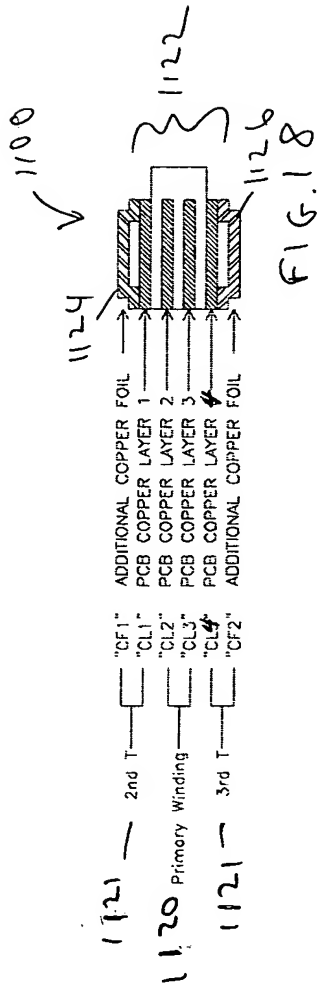


FIG. 14



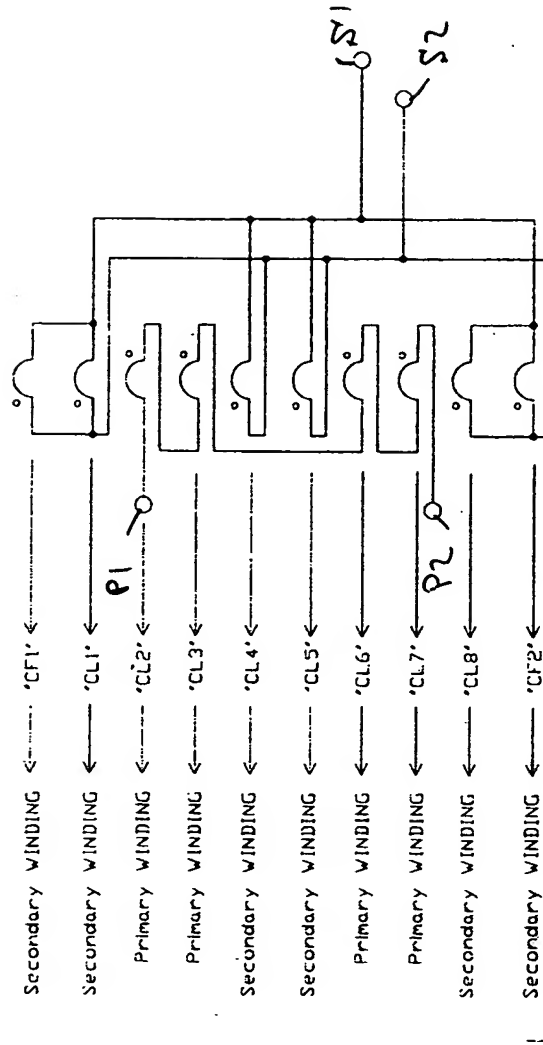


FIG. 20